Dylan Weber

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Forward thinking Purdue University computer engineering graduate specializing in computer architecture and embedded devices. Able to think creatively to deliver solutions through continuous improvement. Possesses great attention to detail and high-quality work, managing concurrent projects in a fast-paced, deadline driven environment.

Technical Expertise

Design Languages
SystemVerilog, Verilog

Programming Languages & Experience
C, C++, Objective-C, Python, Java, Bash, HTML, Javascript, CSS, Node.js, ARM Assembly, Matlab

Microcontroller Experience
Renesas RL, Atmel ATmega/ATtiny, STMicroelectronics STM32, Espressif ESP8266, Espressif ESPS32

Software Experience
KiCad, Mentor Graphics ModelSim, Linux, GTK, wxWidgets, PKCS#11, CMake, OpenGL, Vulkan

Education

Purdue University — *West Lafayette, IN*   August 2016 - August 2020
Bachelor of Science in Computer Engineering

- Member of Purdue Linux Users Group.

Work Experience

Cybersecurity Software Engineer — *Nidec Mobility — St. Charles, IL*   Jan. 2021 - Present
- Created and coordinated cybersecurity policies & procedures for software design & implementation.
- Implemented security-oriented HW & SW for both frontend, backend, and embedded applications.
- Received training for usage and administration of hardware security module appliances.

IT Computer Specialist — *ARCOA Group — Waukegan, IL*   May 2018 - July 2018
- Sorted incoming functional computers and dismantled for components. Tested assets for teardown, cleaned hard drives and swiped sensitive information.
- Troubleshot individual components and replaced one at a time to diagnose problem. Repaired and cleaned salvageable assets and prepared for resale.
- Researched equipment value and verified if equipment remained viable.
- Managed inventory, adding bar codes and recorded into system for tracking.

Computer Engineering Projects

Handheld LIDAR Measurement Device — *Purdue University — West Lafayette, IN*   2020
- Designed circuit board with microcontroller, connecting to accelerometers and gyroscopes to observe data for measurement.
- Set design limitations from anticipated use, constructed prototype, and tested in small parts to ensure functioning design.

Dual Core Cache-Coherent Processor — *Purdue University — West Lafayette, IN*   2019
- Worked collaboratively with partner to create a dual-core CPU with separate cache per core design in SystemVerilog.
- Debugged using Mentor Graphics ModelSim & synthesized on an FPGA.
- Designed each facet of processor and broke down design in order to increase speed.

Wi-Fi-based Analog Receiver & Transmitter — *Highland Park, IL*   2019

Other Work Experience

Teaching Assistant — *Camp Lincolnshire — Lincolnshire, IL*   Summers 2013 - 2017